Current-mode universal biquadratic filter with five inputs and one output using three ICCIIs

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A current-mode universal biquadratic filter with five inputs and one output employing three inverting second-generation current conveyors (ICCIIs), two grounded capacitors and two resistors is presented. The new circuit offers the following advantage features: very low active and passive sensitivities, using of grounded capacitors that is ideal for integrated circuit implementation, the versatility to synthesize any type of active filter transfer functions without requirements for critical component matching conditions, very high output impedance and needs not another current follower to duplicate the input current signal for the realization of notch or allpass responses.

Keywords: Active filter, Analog signal processing, Current-mode, Current conveyors

1 Introduction
There is a growing interest in designing current-mode current conveyor based active filters. This is attributed to their high signal bandwidths, greater linearity and larger dynamic range\(^1\). Several current-mode universal biquadratic filters with multi-inputs and single output were presented\(^2\)-\(^7\). In 1991, Chang and Chen\(^2\) proposed a current-mode universal biquadratic filter with three inputs and single output using five second-generation current conveyors (CCIIs), two grounded capacitors and six grounded resistors. In 1994, Chang \textit{et al.}\(^3\) proposed the second current-mode universal biquadratic filter with three inputs and single output using four current conveyors (two first-generation current conveyors (CCIIs) and two CCIIs), two grounded capacitors and two grounded resistors\(^3\). Chang\(^4\) proposed the third current-mode universal biquadratic filter with three inputs and single output using five plus-type CCIIs, two grounded capacitors and four resistors. Chang and Tu\(^5\) proposed the fourth current-mode universal biquadratic filter with three inputs and single output using four multiple output CCIIs, two grounded capacitors and four grounded resistors. Gunes \textit{et al.}\(^6\) proposed a current-mode universal biquadratic filter with four inputs and single output using three multi-output CCIIs, two grounded capacitors and two grounded resistors. In 2004, Abuelma’atti \textit{et al.}\(^7\) proposed a mixed-mode universal biquadratic filter. This mixed-mode filter also has three current input signals and one current output signal using seven CCIIs, eight resistors and two grounded capacitors. However, the active or passive components used in the design of the multi-inputs and single output current-mode universal filters in Refs 2-5 and 7 were not minimum. Moreover, because these circuits\(^2\)-\(^7\) require two or three current input signals for realizing the notch or allpass filters, therefore, other current followers are needed to duplicate the input current signals for the realization of notch or allpass responses.

Inverting second-generation current conveyor\(^8\) (ICCII) was proposed in 1999 and has been found useful in many applications\(^9\)-\(^13\). In the present paper, a new current-mode universal biquadratic filter with five inputs and one output using three ICCIIs, two grounded capacitors and two resistors is presented. The new circuit offers the following advantage features: very low active and passive sensitivities, using of grounded capacitors that is attractive for integrated circuit implementation\(^14\), the versatility to synthesize any type of active filter transfer functions, without requirements for critical component matching conditions and very high output impedance. Moreover, the proposed circuit does not need another current follower to duplicate the input current signal for the realization of notch or allpass responses with respect to the previous\(^2\)-\(^7\) multi-inputs current-mode filters.

2 Proposed Circuit
The terminal characteristic of the ICCII can be described by the following matrix\(^8\) equation:
\[
\begin{bmatrix}
i_x \\ v_y \\ i_z
\end{bmatrix} =
\begin{bmatrix}
0 & 0 & 0 \\
-1 & 0 & 0 \\
0 & 0 & \pm 1
\end{bmatrix}
\begin{bmatrix}
v_x \\ v_z \\ i_x
\end{bmatrix}
\]

\[ \cdots (1) \]

The proposed current-mode universal biquadratic filter is shown in Fig. 1. The circuit with five input terminals and one output terminal comprises three ICCII's, two grounded capacitors and two resistors. The use of grounded capacitors is particularly attractive for integrated circuit implementation. Circuit analysis yields the following output current \( I_{\text{out}} \):

\[
I_{\text{out}} = \frac{s^2C_1C_2(I_{\text{in}3} + I_{\text{in}4} + I_{\text{in}5}) + sC_2G_1(I_{\text{in}2} - I_{\text{in}5}) + G_2G_3(I_{\text{in}1} + I_{\text{in}4} + I_{\text{in}5})}{s^2C_1C_2 + sC_2G_1 + G_1G_2} \]

\[ \cdots (2) \]

In Eq. (2), \( s \) represents a complex variable indicating frequency. Specializations of the numerator in Eq. (2) result in the following filter functions:

(i) If \( I_{\text{in}1} = I_{\text{in}2} = I_{\text{in}4} = I_{\text{in}5} = 0 \) (opened), a highpass filter can be obtained with \( I_{\text{out}}/I_{\text{in}3} \); (ii) If \( I_{\text{in}1} = I_{\text{in}3} = I_{\text{in}4} = I_{\text{in}5} = 0 \) (opened), a bandpass filter can be obtained with \( I_{\text{out}}/I_{\text{in}2} \); (iii) If \( I_{\text{in}2} = I_{\text{in}3} = I_{\text{in}4} = I_{\text{in}5} = 0 \) (opened), a lowpass filter can be obtained with \( I_{\text{out}}/I_{\text{in}1} \); (iv) If \( I_{\text{in}1} = I_{\text{in}2} = I_{\text{in}3} = I_{\text{in}5} = 0 \) (opened), a notch filter can be obtained with \( I_{\text{out}}/I_{\text{in}4} \); (v) If \( I_{\text{in}1} = I_{\text{in}2} = I_{\text{in}3} = I_{\text{in}4} = 0 \) (opened), an allpass filter can be obtained with \( I_{\text{out}}/I_{\text{in}5} \).

Note that no critical component matching conditions are required for the realization of each filter response. Moreover, each filter response can be obtained with only one input current signal. Taking into account the non-idealities of the ICCII, the characteristics of the non-ideal ICCII can be given by \( i_i = \alpha + \varepsilon_i i_i \) and \( v_i = -\beta v_y \), where \( \alpha = 1 - \varepsilon \) and \( \varepsilon_i \) denotes the current tracking error and \( \beta = 1 - \varepsilon_i \) and \( \varepsilon_i \) denotes the voltage tracking error of the ICCII. The denominator of the non-ideal voltage transfer function for Fig. 1 is

\[
D(s) = s^2C_1C_2 + sC_2G_1\alpha_i\alpha_3\beta_1 + G_1G_2\alpha_1\beta_1\beta_3(\alpha_{21} + \alpha_{22}\alpha_{31} - \alpha_{31}) \]

\[ \cdots (3) \]

The resonance angular frequency \( \omega_0 \) and quality factor \( Q \) are obtained as:

\[
\omega_0 = \frac{G_2G_3\alpha_1\beta_1(\alpha_{21} + \alpha_{22}\alpha_{31} - \alpha_{31})}{C_1C_2} \]

\[ \cdots (4) \]

\[
Q = \frac{1}{\alpha_{12}\alpha_{31}}\frac{G_2G_3\alpha_1\beta_1(\alpha_{21} + \alpha_{22}\alpha_{31} - \alpha_{31})}{C_2G_1\beta_1} \]

\[ \cdots (5) \]

The active and passive sensitivities of \( \omega_0 \) and \( Q \) are:

\[
S_{\alpha_1}^{\omega_0} = -S_{\alpha_2}^{\omega_0} = S_{\alpha_3}^{\omega_0} = \frac{1}{2}, \quad S_{\alpha_1, \alpha_2}^{\omega_0} = \frac{1}{2},
\]

\[
S_{\alpha_1}^{Q} = 0, \quad S_{\alpha_2}^{Q} = -S_{\alpha_3}^{Q} = S_{\alpha_1, \beta}^{Q} = -S_{\beta_1}^{Q} = \frac{1}{2},
\]

\[
S_{\alpha_1, \alpha_2}^{Q} = -1, \quad S_{\alpha_2, \alpha_2}^{Q} = \frac{1}{2}, \quad S_{\alpha_3}^{Q} = -1.
\]

These values are calculated by assuming that \( \alpha \) and \( \beta \) are near to unity. The sensitivities of \( \omega_0 \) and \( Q \) are very small.

### 3 Simulation Results

To verify the theoretical analysis, HSPICE simulations were carried out to demonstrate the feasibility of the proposed circuit in Fig. 1. The ICCII was realized by the CMOS implementation of Fig. 5 in Elwan and Soliman and is redrawn in Fig. 2. The simulations use 0.18 µm level 49 MOSFET from TSMC.

Fig. 1 — Proposed current-mode universal biquadratic filter

Fig. 2 — CMOS realization of the ICCII
The dimensions of the NMOS transistors in the ICCIs are set to be $W = 4.5 \, \mu m$ and $L = 0.9 \, \mu m$. The dimensions of the PMOS transistors in the ICCIs are set to be $W = 9 \, \mu m$ and $L = 0.9 \, \mu m$.

Figure 3(a-e) show the simulated frequency responses for the highpass ($I_{in3}$), bandpass ($I_{in2}$), lowpass ($I_{in1}$), notch ($I_{in4}$) and allpass ($I_{in5}$) filters of Fig. 1, respectively, designed with $Q = 1$ and $f_o = 1.8947 \, MHz$: $C_1 = C_2 = 30 \, pF$ and $R_1 = R_2 = 2.8 \, k\Omega$.

Fig. 3 — Simulated frequency responses of Fig. 1 design with $C_1 = C_2 = 30 \, pF$ and $R_1 = R_2 = 2.8 \, k\Omega$. (a) highpass filter ($I_{in3}$), (b) bandpass filter ($I_{in2}$), (c) lowpass filter ($I_{in1}$), (d) notch filter ($I_{in4}$), (e) allpass filter ($I_{in5}$)
The supply voltages are $V_+ = +1.25\, \text{V}$, $V_- = -1.25\, \text{V}$, $V_{b1} = -0.45\, \text{V}$ and $V_{b2} = +0.3\, \text{V}$. In Fig. 3, the solid line and dash line are the ideal gain and phase frequency responses, respectively, obtained from Eq. (2). The characteristics “o” and “x” are the simulated gain and phase frequency responses, respectively, from HSPICE. Fig. 4 shows the simulated gain frequency responses for the bandpass filter of Fig. 1 as the capacitance $C_1$ is varied with $C_2 = 30\, \text{pF}$ and $R_1 = R_2 = 2.8\, \text{k}\, \Omega$. Fig. 5 shows the simulated input and output referred noise spectral densities at the z32- node of the ICCII(3) for the allpass filter in Fig. 1 design with $2\, \text{mA}_{p-p}$ input current signal, $C_1 = C_2 = 30\, \text{pF}$, $R_1 = R_2 = 2.8\, \text{k}\, \Omega$ and the output terminal (z32-) connected to a $1\, \text{k}\, \Omega$ resistor. From Fig. 5, the input and output referred noise spectral densities at the output node of Fig. 1 are far below the ideal allpass responses magnitude (1V).

4 Conclusions

Several multi-inputs and single output current-mode universal biquadratic filters were proposed\textsuperscript{2-7}. However, because these circuits require two or three current input signals in the realizations of the notch or allpass filters, one more active component is needed in each of these circuits to duplicate the input current signal for the realizations of notch or allpass responses. In the present paper, a new universal active current filter with five inputs and single output using three ICCIIIs, two grounded capacitors and two resistors is presented. The proposed circuit offers several advantages, such as very low filter sensitivity to active and passive components, the use of grounded capacitors, the versatility to synthesize any type of active filter transfer functions, without requirements for critical component matching conditions, very high output impedance and needs not one more current follower to duplicate the input current signal in the realizations of notch or allpass filters.

References